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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/634,596	08/04/2003	Johnny Chan	ATM-253	6529
3897	7590	09/09/2004	EXAMINER	
SCHNECK & SCHNECK P.O. BOX 2-E SAN JOSE, CA 95109-0005			WAMBACH, MARGARET R	
			ART UNIT	PAPER NUMBER
			2816	

DATE MAILED: 09/09/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/634,596

Applicant(s)

CHAN ET AL.

Examiner

Margaret R Wambach

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-22 is/are pending in the application.
4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 14-18 is/are allowed.
- 6) ☒ Claim(s) 1-4, 13, 19 and 20 is/are rejected.
- 7) ☒ Claim(s) 5-12, 21 and 22 is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 04 August 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. ____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____. |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>11/10/03</u> . | 6) <input type="checkbox"/> Other: ____. |

DETAILED ACTION

Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1-4, 13, 19 and 20 are rejected under 35 U.S.C. 102(b) as being clearly anticipated by Iida et al. (hereafter "Iida")

Referring to Figure 2 of Iida, a shift register device is taught as recited in claims 1, 19 and 20 including a bit data input line (IN) along which bit data is to be shifted toward an output terminal (OUT);

latches in succession connected in series along said bit data input line (insofar as a latch is a device which holds the last value of its input until enabled to acquire a new value, 12 and 14 comprise latches and they are connected as recited in claim 1);

transistor pass gates (Q3 and Q4); and

control signal input lines (the gate inputs of Q3 and Q4) connected to said transistor pass gates, each control signal input line operable to provide a control signal to one of said transistor pass gates to shift data along said bit data input line wherein the control signals are applied in a staggered time pattern (referring to Figures 3A and 3B and assuming staggered means that each positive pulse is applied one at a time as described in the specification, we can see that there is no overlap of control pulses.)

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With regard to claim 2, Figures 3C-3H demonstrate that bit data is shifted from a preceding latch (12) to a succeeding latch (14).

With regard to claim 3, Figures 3C-3H demonstrate that bit data is shifted from a preceding latch (12) to a position external to said latches (OUT.)

With regard to claim 4, Figures 3C-3H demonstrate that bit data is shifted from a position external to the latches (IN) to a succeeding latch (12.)

With regard to claims 13 and 19, from Figures 3A and 3B we can see that a positive control pulse is first applied to the last transistor pass gate. Similarly, with regard to claim 19, assuming that "reverse succession" means that pulses are applied at the last stage before the first. This is shown by Figures 3A and 3B but, it should be noted, that with a continuous pulse train rather than individual pulses, the opposite case is true as well. To overcome the foregoing rejections, it is recommended that applicant amend the claims to stipulate that isolated pulses that are not part of a periodic signal are applied to the control lines.

Allowable Subject Matter

Claims 14-18 are allowed.

Claims 5-12, 21 and 22 are objected to as being dependent upon a rejected base claim, but would be allowable if rewritten in independent form including all of the limitations of the base claim and any intervening claims.

Conclusion

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The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Imai does show four separate control lines with distinct control signals but since the pulses overlap and staggered is being read as meaning "one at a time," among other reasons, Imai fails to meet the limitations of the independent claims.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Margaret R Wambach whose telephone number is (571)272- 1756. The examiner can normally be reached on Monday, Tuesday, Thursday and Friday 6am-4:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on (571)272-1740. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Margaret R Wambach
Primary Examiner
Art Unit 2816

mrw